FIG. 1 (PRIOR ART)

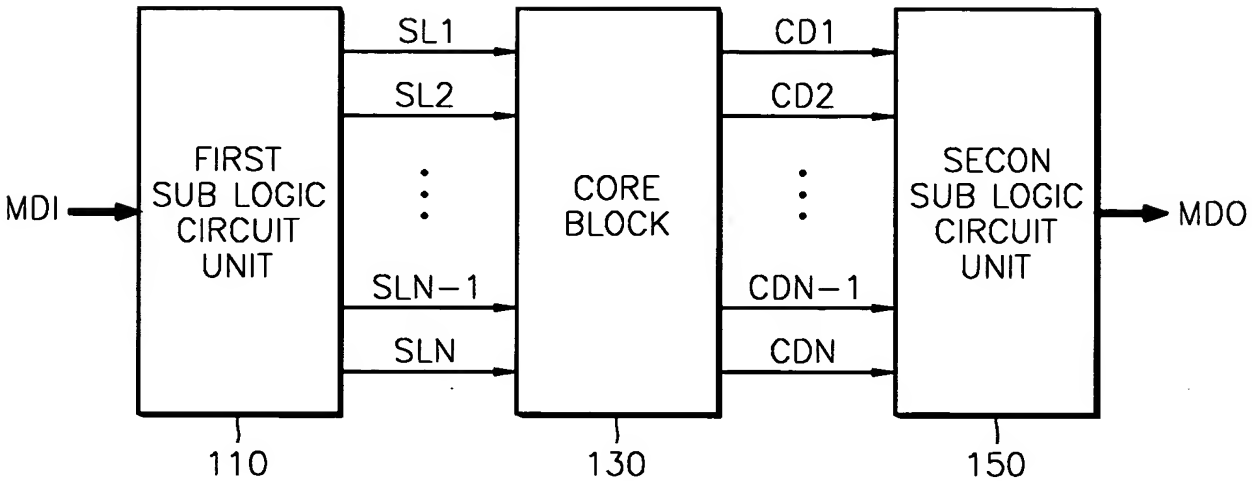


FIG. 2 (PRIOR ART)

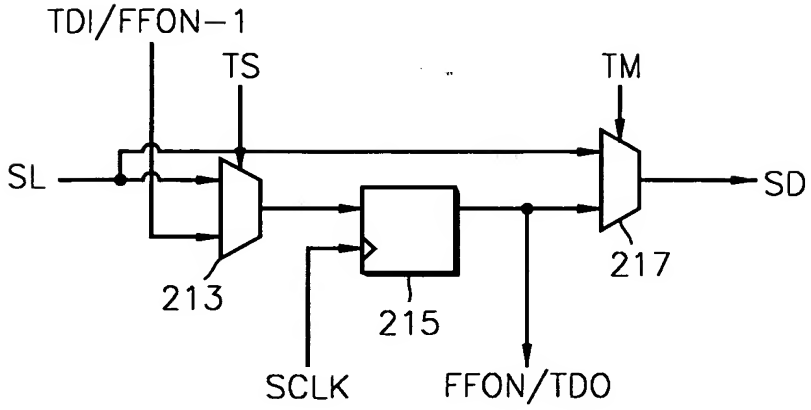


FIG. 3 (PRIOR ART)

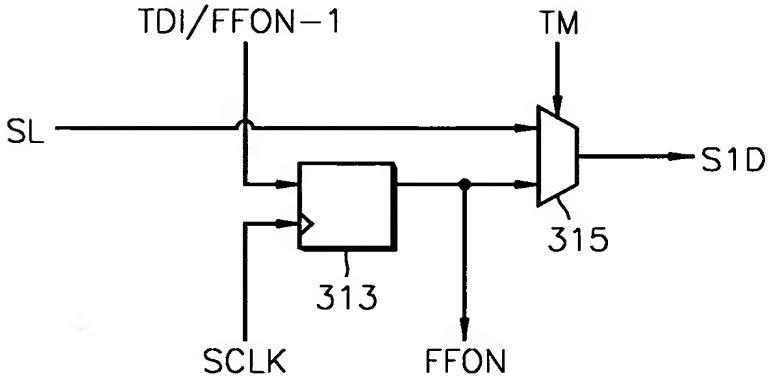


FIG. 4 (PRIOR ART)

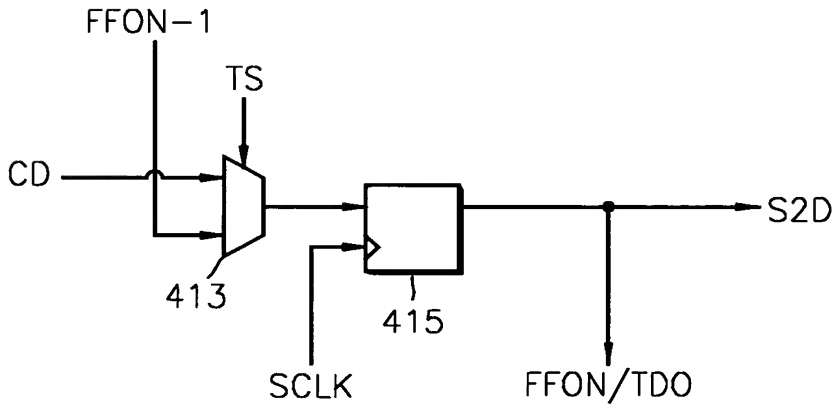


FIG. 5

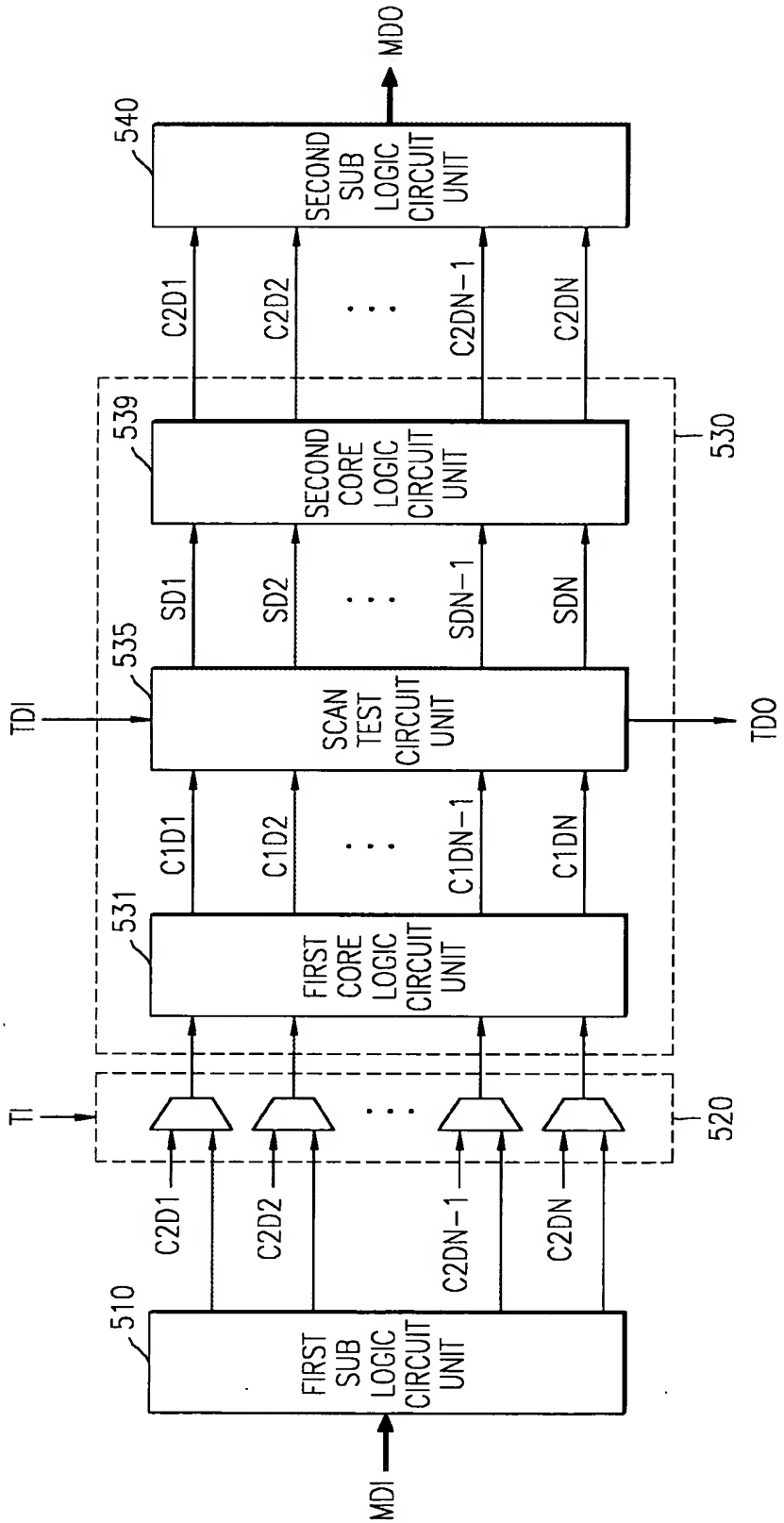
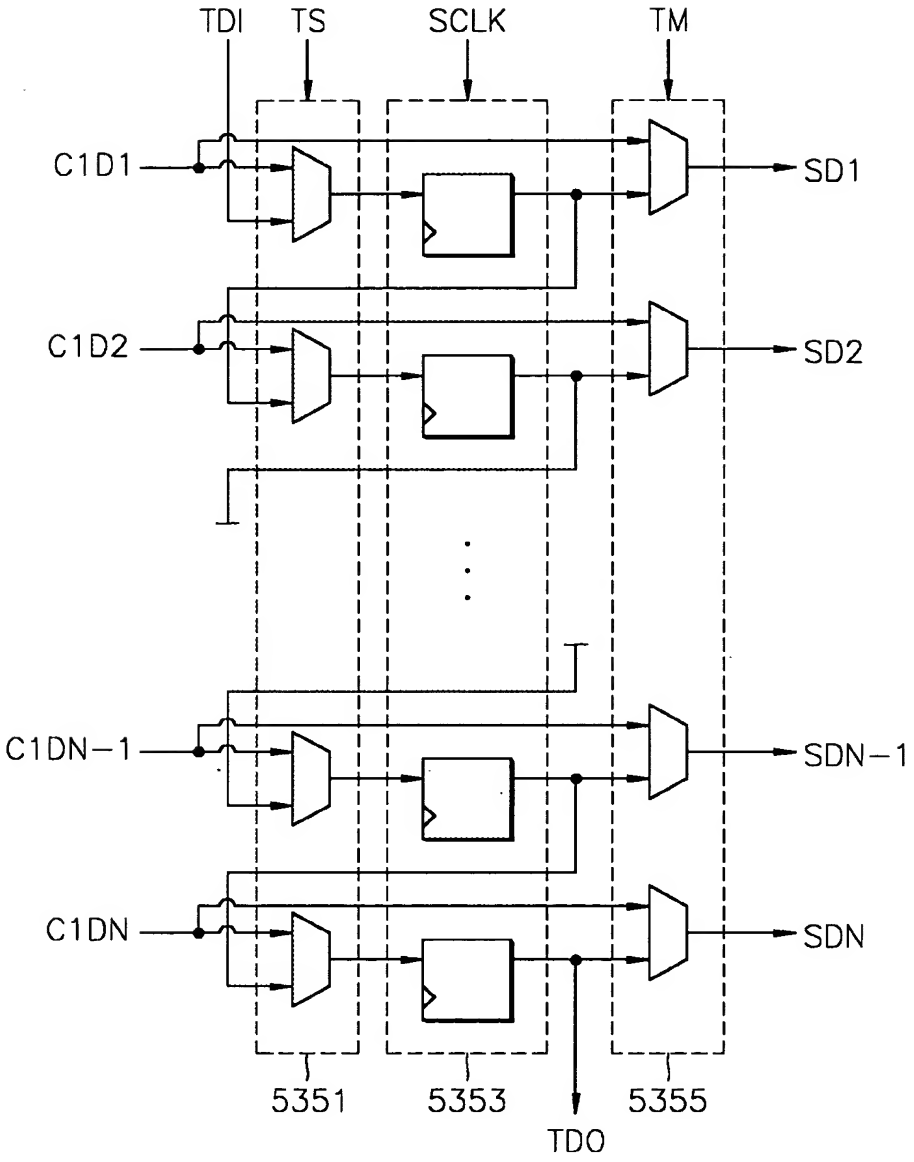

MDI
510
FIRST SUB LOGIC CIRCUIT UNIT
C2D1
C2D2
C2DN-1
C2DN
TI
520
531
FIRST CORE LOGIC CIRCUIT UNIT
C1D1
C1D2
C1DN-1
C1DN
TDI
535
SCAN TEST CIRCUIT UNIT
TDO
SD1
SD2
SDN-1
SDN
530
539
SECOND CORE LOGIC CIRCUIT UNIT
C2D1
C2D2
C2DN-1
C2DN
540
SECOND SUB LOGIC CIRCUIT UNIT
MDO

FIG. 6


TDI
TS
SCLK
TM
C1D1
SD1
C1D2
SD2
C1DN−1
SDN−1
C1DN
SDN
5351
5353
5355
TDO